88888888888888888888888888888888888888	000000000 000000000	00000000 00000000 00000000		\$
888 888 888 888 888 888	000 000 000 000 000 000 000 000	000 000 000 000 000 000		\$\$\$ \$\$\$ \$\$\$ \$\$\$
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88888888888888888888888888888888888888	000 000 000 000 000 000	000 000 000 000 000 000	††† ††† †††	\$\$\$\$\$\$\$\$\$ \$\$\$ \$\$\$ \$\$\$ \$\$\$
888 888 888 888 888	000 000 000 000 000	000 000 000 000		SSS
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MM MM MMM MMM MMMM MMM MM MM MM MM MM MM	AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA	RRRRRRRR RRRRRRRR RR RR RR RR RR RR RRRRRR			

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.TITLE BOOTS MACROS

BOOTS

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: FACILITY:

ABSTRACT:

This module contains macros for the BOOTS facility

ENVIRONMENT:

AUTHOR: STEVE BECKHARDT, CREATION DATE: 31-Oct-1979

;***********************************

MODIFIED BY:

V03-005 KDM0073 Kathleen D. Morse 22-Aug-1983 Change TIMEDWAIT macro to include load address for VMB (e.g., address of RPB).

V03-004 KDM0058 Kathleen D. Morse 13-Jul-1983 Add boot-time specific TIMEDWAIT macro for boot drivers.

V03-003 KTA3058 Kerbey T. Altmann 20-Jun-1983 Add cell for boot device name (may be different from driver name!). Also unit disconnect routine.

V03-002 KTA3034 Kerbey T. Altmann 02-Feb-1983 Add cell for booting node name.

.SBTTL DECLARATIONS

INCLUDE FILES:

MACROS:

\$BOOT_DRIVER MACRO - SETS UP A TABLE ENTRY FOR A BOOT DEVICE DRIVER. EACH TABLE ENTRY CONTAINS:

CPUTYPE CPU TYPE. DEFAULT = -1 (DON'T CARE)

DEVTYPE BOOT DEVICE TYPE VALUE. DEFAULT = -1 (DON'T CARE)

ACTION ROUTINE ADDRESS (ACTUALLY OFFSET FROM START OF TABLE ENTRY). DEFAULT = 0 (NONE). ACTION

SIZE SIZE OF ENTIRE DRIVER IN BYTES. CANNOT BE DEFAULTED.

ADDRESS OF DRIVER (ACTUALLY OFFSET FROM START OF TABLE ENTRY). CANNOT BE DEFAULTED. ADDR

ADDRESS OF DRIVER ENTRY POINT (ACTUALLY OFFSET FROM ADDRESS OF DRIVER). DEFAULT = 0 (ADDRESS OF DRIVER AND ENTRY POINT ARE THE SAME). ENTRY

ADDRESS OF DRIVER NAME IN .ASCIC. (ACTUALLY OFFSET FROM ADDRESS OF DRIVER). CANNOT BE DRIVRNAME DEFAULTED.

AUXDRNAME AUXDRNAME ROUTINE ADDRESS (ACTUALLY OFFSET FROM START OF TABLE ENTRY). DEFAULT = 0 (NONE).

UNIT_INIT ROUTINE ADDRESS (ACTUALLY OFFSET FROM START OF TABLE ENTRY). DEFAULT = 0 (NONE). UNIT_INIT

UNIT_DISC UNIT_DISCONNECT ROUTINE ADDRESS (ACTUALLY OFFSET FROM START OF TABLE ENTRY). DEFAULT = 0 (NONE).

BOOT DEVICE NAME ADDRESS (ACTUALLY OFFSET FROM START OF TABLE ENTRY). DEFAULT = FIRST TWO DEVNAME LETTERS OF DRIVENAME.

CPUTYPE=-1, DEVTYPE=-1, ACTION, SIZE, ADDR, -ENTRY, DRIVANAME, AUXDRNAME, UNIT_INIT, -.MACRO \$BOOT_DRIVER UNIT_DISC, DEVNAME

.PSECT BOOTDRIVR_4 STABLE =.

. WORD CPUTYPE CPUTYPE EQ .ERROR

: CPU TYPE CANNOT BE 0 : .ENDC

. WORD DEVTYPE

```
ACTION
.LONG
.LONG
.ENDC
.LONG
        ACTION-STABLE
        SIZE
ADDR=STABLE
.LONG
                 ENTRY
.LONG
.LONG
        ENTRY-ADDR
.LONG
        DRIVRNAME-ADDR
                  AUXDRNAME
.LONG
.LONG
        AUXDRNAME-ADDR
                 UNIT_INIT
.LONG
.LONG
        UNIT_INIT-ADDR
                 UNIT_DISC
.LONG
.LONG
        UNIT_DISC-ADDR
.ENDC
. IF
                 DEVNAME
.LONG
        DRIVRNAME-ADDR+1
.LONG
        DEVNAME-ADDR
.ENDC
        BOOTDRIVE 2
$BOOT_DRIVER
.PSECT
. ENDM
```

Define the offsets into the argument list passed by VMB to SYSBOOT

.MACRO \$VMBARGDEF,GBL

SDEFINI VMB, GBL, 4

```
VMB$Q_FILECACHE
VMB$L_LO_PFN
VMB$L_HI_PFN
VMB$Q_PFRMAP
VMB$Q_UCODE
VMB$B_SYSTEMID
SDEF
SDEF
SDEF
                                                                                                                     FILEREAD Cache Descriptor
Lowest PFN found by VMB
                                                                             .BLKQ
                                                                             .BLKL
                                                                                                                      Highest PfN exclusive
PfN Bitmap descriptor
Loaded ucode descriptor
48 bit SCS systemid
                                                                             .BLKL
                                                                            .BLKQ
.BLKB
$DEF
$DEF
                                                                                                                       Spare
SDEF
SDEF
SDEF
SDEF
SEQU
                   VMB$L_FLAGS
VMB$L_CI_HIPFN
VMB$Q_NODENAME
VMB$C_ARGBYTCNT
                                                                                                                      Word of flags
Highest PFN used by CI code
                                                                             .BLKL
                                                                             .BLKL
                                                                                                                  : Booting node name
: Size of argument list in bytes
: Flag to SYSBOOT to load SCS
                                                                             .BLKQ
                   VMB$V_LOAD_SCS
```

SDEFEND VMB, GBL, ARGDEF

.ENDM **SVMBARGDEF**

TIMEDWAIT - Timed Wait Loop with Imbedded Tests

Macro to wait for a specified interval of time. Uses a processor specific value established by system bootstrap to determine an approximate interval of time to wait instead of reading the processor clock. Instructions that test for various exit conditions may be imbedded within the wait loop, if so desired.

This version of TIMEDWAIT is set up to be used with boot drivers. It contains the right kind of PIC references to EXESGL_TENUSEC and EXESGL_UBDELAY, for code that is moved within the address space of an image at run-time instead of remaining bound to the relative offset within the image given it at link-time. (Note that these two counters are kept in the BQO structure and are referenced via BQO\$L_UBDELAY and BQO\$L_TENUSEC.)

INPUTS:

TIME - the number of 10 micro-second intervals to wait IME - the number of 10 micro-second intervals to wait INS1 - first instruction to imbed within wait loop INS2 - second instruction to imbed within wait loop INS3 - third instruction to imbed within wait loop INS4 - fourth instruction to imbed within wait loop INS5 - fifth instruction to imbed within wait loop INS6 - sixth instruction to imbed within wait loop DONELBL - label for exit from wait loop IMBEDLBL - Label for imbedded instructions in wait loop UBLBL - Label for UNIBUS SOBGTR loop

OUTPUTS:

RO - indicates success of failure status. Success is defined as the bit being at the specified sense within the specified time interval.

R1 - destroyed, all other registers preserved.

.MACRO TIMEDWAIT TIME, INS1, INS2, INS3, INS4, INS5, INS6, DONELBL, ?IMBEDLBL, ?UBLBL

.nlist cnd RPB\$L IOVEC(R9),R1 ; Get address of IOV
TIME,BQO\$L TENUSEC(R1),R1 ; Calculate time.
#SS\$_NORMAE,R0 ; Assume success.
-(SP) ; Reserve space for Get address of IOVEC data cells. MOVL MULL 3 MOVZWL CLRL IMBEDLBL: INS1 : Reserve space for delay loop index.

:--

'INS2' 'INS3'

```
BOOTS.MAR;1

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'INS6'
ADDL3 RPB$L IOVEC(R9), -
MBQO$L UBDELAY,(SP)
BOOTS.MAR;1

WBQO$L UBDELAY,(SP)
BOOTS.MAR;1

INS6'
ADDL3 RPB$L IOVEC(R9), -
WBQO$L UBDELAY,(SP)
BOOTS.MAR;1

WBQO$L UDVEC(R9), -
WBQO$L UBDELAY,(SP)
BOOTS.MAR;1

WBQO$L UBDELAY,(SP)

BOOTS.MAR;1

Get address of IOVEC data cells
Holding delay loop count itself.
Get delay loop count itself.
Delay loop to slow bit tests down
Holding delay loop to slow b
```

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